

6301 PROM to 2716 EPROM Pin Mapping (by RCL9, May 8 2026)

6301, PROM #8C on Morrow DJ2D-B Rev 2 -- 4 x 256 , 16 pin

This mapping was used to read the PROM on a XGecu T48 EPROM reader as a 2716

<u>6301</u>	<u>2716 EPROM</u>	
1 - IN -- A6 -- MEMORY	2 - A6	
2 - IN -- A5 -- A15	3 - A5	
3 - IN -- A4 -- A14	4 - A4	
4 - IN -- A3 -- A13	5 - A3	
5 - IN -- A0 -- A10	8 - A0	
6 - IN -- A1 -- A11	7 - A1	
7 - IN -- A2 -- A12	6 - A2	
8 - Gnd	12 - Gnd	
9 - OUT -- D3 -- /SELECT	13 - Q3	Pull-up to +5v
10 - OUT -- D2 -- ROM I/O	11 - Q2	Pull-up to +5v
11 - OUT -- D1 -- RAM	10 - Q1	Pull-up to +5v
12 - OUT -- D0 -- BD SELECT	9 - Q0	Pull-up to +5v
13 - IN -- /CE1	18 - /CE	
14 - IN -- /CE2	18 - /CE	
15 - IN -- A7 -- BOARD ENBL	1 - A7	
16 - Vcc	24 - Vcc	
	14 - Q4	Gnd
	15 - Q5	Gnd
	16 - Q6	Gnd
	17 - Q7	Gnd
	19 - A10	
	20 - /G (/OE)	
	21 - Vpp	Programming supply
	22 - A9	
	23 - A8	

2716 Pinouts:

1	A7 - Address Input
2	A6 - Address Input
3	A5 - Address Input
4	A4 - Address Input
5	A3 - Address Input
6	A2 - Address Input
7	A1 - Address Input
8	A0 - Address Input
9	Q0 - Data Input
10	Q1 - Data Input
11	Q2 - Data Input
12	Vss - Ground
13	Q3 - Data Input
14	Q4 - Data Input
15	Q5 - Data Input
16	Q6 - Data Input
17	Q7 - Data Input
18	EP - Enable Programming
19	A10 - Address Input
20	G - Output Enable
21	Vpp - Programming Supply
22	A9 - Address Input
23	A8 - Address Input
24	Vcc - Positive Power Supply

Note: this chip is indeed open-collector, tied high with 300 ohm resistors